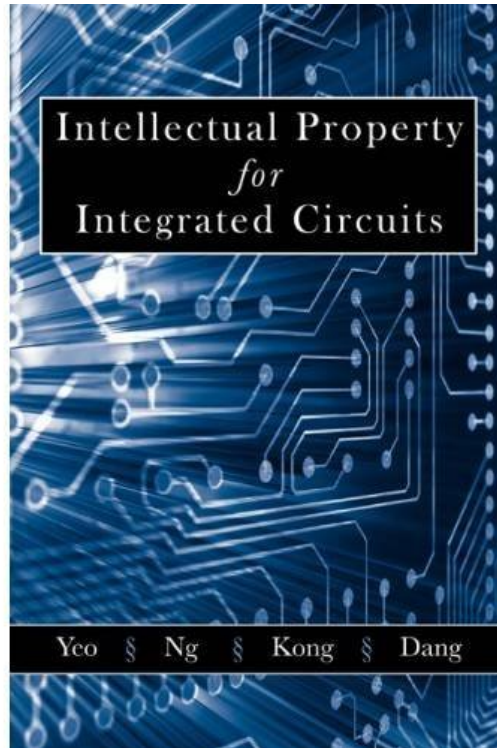


# Intellectual Property for Integrated Circuits

*Kiat Seng Yeo, Kim Tean Ng, Zhi Hui Kong*  
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Intellectual Property for Integrated Circuits provides integrated circuit designers with the know-how to effectively search for and interpret prior art. Patent attorneys are paid by the hour, therefore if inventors have a working knowledge of IP law, as well as how and where to search for prior art, they can significantly reduce the number of hours spent with patent attorneys. At the same time it will ensure that both inventors and patent attorneys understand one another. This book will give integrated circuit (IC) inventors a substantial understanding of the numerous forms of intellectual property and their underlying laws and governing principles to efficiently protect and exploit their own inventions and benefit financially from those rights. Key Features --Equips the IC designer with the knowledge to effectively search for and interpret prior art --Explains technical contents of semiconductor and IC design in an interesting and non-technical manner making it valuable to IP practitioners --Addresses the legal knowledge needed by IC inventors to avoid the risk of IP infringement litigation --Illustrates concepts through case studies and examples, and includes crucial and valuable search links --WAV offers an online version of the in-chapter references with helpful links to governmental agencies and articles to further learning and research available from the Web Added Value Download Resource Center at [jrosspub.com](http://jrosspub.com)

About the Author Dr. Kiat-Seng Yeo is currently Head of Division of Circuits and Systems and Interim Director of the new IC Design Centre of Excellence at the School of Electrical and Electronic Engineering at Nanyang Technological University (NTU) in Singapore. Prior to these appointment, he was Program Manager of the System-on-Chip flagship project, Coordinator of the Integrated Circuit Design research group, and Principal Investigator of the Integrated Circuit Technology research group at NTU. He is currently a board member of Microelectronics IC Design and Systems Association of Singapore (MIDAS), a member of the Advisory Committee of the Centre for Science Research Talent Development of Hwa Chong Institution, Chairman of the Advisory Committee of Dazhong Primary School, and consultant/advisor to several statutory boards and multinational corporations in the areas of semiconductor devices, electronics, and integrated circuit design. Professor Yeo is the author of four books, more than 300 journal and conference articles, and has been granted 17 patents. His research interests include device characterization and modeling, RF IC design, and low-voltage low-power IC design.